



Talent Program as Cleanroom Engineer: Fabrication of Photonic Devices (100%)

Join the Talent Program of Versics AG, a deep-tech spin-off from ETH Zurich founded in 2022.

We manufacture photonic microchips and package them into innovative tech products. That means we are a vertical integrator of optical devices, having complete control and freedom from the initial product design to its microchip fabrication, assembly, and testing until the shipping process.

We seek an engineer to join our Talent Program, which lasts 2 years. The Talent Program consists of thorough skills learning and maturing in microfabrication know-how, cleanroom processes, and material science. The reinforced skills have a direct impact on our photonic chips for telecommunications. Your role will evolve within the company after two years.

We are already working with telecom market leaders to improve how we exchange data on Earth and in Space - would you miss this opportunity?

The Opportunity

- Work with state-of-the-art cleanroom machinery (lithography, etching, deposition, characterization, back-end processing, etc) and operate them yourself
- Have a central role in the fabrication of our low-power integrated optic devices, which operate beyond 110 GHz of bandwidth
- Streamline the current fabrication flows and implement leaner approaches
- Optimize wafer-scale processes to produce our products at scale
- Maximize device yield by having constant communication with the assembly team

Your Profile

- Motivation to work in a cleanroom environment
- Commitment to work in a purpose-driven team
- Eager to learn new hard and soft skills
- High sense of persistence and quality at work
- Knowledge in material science and chemistry, particularly applied to cleanroom processes
- Experience in cleanroom processes (lithographies, etching, depositions, lift-offs, cleaning, bonding, etc)
- Experience in electronic and/or photonic devices is a plus
- Education as B.Sc or M.Sc with cleanroom work and results is a plus
- Hands-on in cleanroom equipment (running the equipment by yourself and, when needed, troubleshooting errors)



- Hands-on in developing your own process recipes (optimizing steps with several parameters, assessing priorities to final quality, etc)
- Hands-on in creating fabrication flows from scratch (following state-of-the-art processes and combining them with in-house capabilities and know-how)
- Good management and reporting practice in your work

What Versics offers

- Fast-track learning and experience in microfabrication processes, workflows and integrated photonic circuits through the Talent Program
- Key member of a dynamic team, where you will gain responsibility within the company
- Creating a real impact on telecommunications with the latest photonic tech
- Collaborating with world-renowned experts in photonic devices
- A workplace in Zurich (2nd city with a higher quality of life worldwide) and with links to ETH Zurich (8th top University in the world in 2024)

Application Procedure

- Publication date: February 24, 2025
- Application deadline: April 6, 2025 (included)
- Documents: Attach your CV (max. 1 page) and motivation letter (max. 1 page). Applications without a motivation letter will not be considered.
- Interviews in calendar weeks 16 (April 14-17) and 17 (April 22-25)
- Starting time: July 2025 or earlier

Please send your application and questions about this job position through the ETH Get Hired platform ([link here](#)).